

PCI Express Interface Modules Digital I/O

Base Specifications

Model type	PEX-H291022	PEX-H292022	PEX-H293022
Bus requirements	PCI Express™ Base Specification Rev. 1.0a x1		
Number of slots	1 slot		
Memory size	8 KB + 32 bytes x2		
Data transfer method	I/O transfer (memory mapped I/O)		
Dimensions	165.00 (D) x 106.65 (H) [mm]		
Weight	110g	110g	115g
Power consumption	+3.3 Vdc (+/-0.3 V):		
All I/O and control signals inactive	0.3 A (typ.)	0.3 A (typ.)	0.3 A (typ.)
All I/O and control signals active	0.7 A (typ.)	0.5 A (typ.)	0.6 A (typ.)
Environmental conditions	Operating temperature: 0 deg C to 50 deg C Relative humidity: 20% to 90% (non-condensing)		
Connectors	CNA: 100-pin 0.8 mm-pitch female connector CNB: 100-pin 0.8 mm-pitch female connector		
On-board connector	HDRA-E100W1LFD1EC-SL+ (Honda Tsushin Kogyo Co., Ltd.) or equivalent		
Acceptable cable connector	HDRA-E100MA1+ (Honda Tsushin Kogyo Co., Ltd.) or equivalent		
Isolation	No-isolation		
Interrupt sources	12 sources		
External I/O Signals	RSTIN (external reset input signal) x 2		

Digital Input / Output Specifications

Model type	PEX-H291022	PEX-H292022	PEX-H293022
Input signals	IN1 to IN128 (bi-direction)		
Input configurations	Schmitt trigger TTL input (with 4.7kΩ pull-up resistor)	Schmitt trigger LVTTTL input (with 4.7kΩ pull-up resistor)	CMOS-gated high-voltage input (with 4.7kΩ pull-up resistor)
Input logic	1 ← Low (on, closed) 0 ← High (off, open)		
Input signal voltage range	+5 Vdc	+3.3 Vdc to +5 Vdc	+5 Vdc to +24 Vdc
Low-level input current	IIL = -1.2mA (max.)	IIL = -0.8mA (max.)	IIL = -1.1mA (max.)
High-level input current	IiH = +15μA (max.)		IiH = +11μA (max.)
Low-level input voltage	VIL = +0.8V (max.)		VIL = +0.7 V (max.)
High-level input voltage	VIH = +2.0V (max.)		VIH = +3.1 V (min.)
Input response time	TRON: 0.1μs (typ.) TROFF: 0.1μs (typ.)		
Output signals	OUT1 to OUT128 (bi-direction)		
Output configurations	TTL open-collector output (with 4.7 kΩ pull-up resistor)	LVTTTL open-collector output (with 4.7 kΩ pull-up resistor)	TTL open-collector output (with 4.7 kΩ pull-up resistor)
Output logic	1 → Low (on, closed) 0 → High (off, open) (default value)		
Output signal voltage range	0 Vdc to +5 Vdc	0 Vdc to +3.3 Vdc	+5 Vdc to +24 Vdc
Output current	IOL = +40 mA (max.)		
Low-level output voltage	VOL = +0.2 V (max.) (IOL = +10 mA) VOL = +0.5 V (max.) (IOL = +40 mA)		
Output response time	TRON: 0.5 μs (typ.) TROFF: 1.0 μs (typ.)		TRON: 1.0 μs (typ.) TROFF: 1.0 μs (typ.)



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