

# PCI Express Interface Modules RS-485(422) HDLC 2ch

## Base Specifications

Model type	PEX-H467102	PEX-H467102P
Bus Requirements	PCI Express™ Base Specification Rev. 1.0a x1	
Number of slots	1 slot	
Memory Size	8 KB + 1MB x 2 + 32 bytes x 2 + 16 bytes (automatically assigned)	
Data transfer method	I/O transfer (memory mapped I/O)	
Dimensions	106.68(D)×106.65(H) [mm]	
Weight	85g	90g
Power consumption	+3.3 Vdc (+/- 9%):0.5A(typ.)	+3.3 Vdc (+/- 9%):0.9A(typ.)
Environmental conditions	Operating temperature: 0 deg C to 50 deg C Relative humidity: 20% to 90% (non-condensing)	
Connectors	CN1, CN2: 15-pin D-sub female connector	
On-board connector	CN1, CN2: 17LE-13150-27(D4BB)A-FA (DDK Ltd.) or equivalent (screw: M3)	
Acceptable cable connector	CN1, CN2: 17JE-23150-02(D1) (DDK Ltd.) or equivalent	
Isolation	No-isolation	Individual channel isolation
Interrupt sources	5 sources: receive buffer overflow, receive buffer overrun, control signals, HDLC status, and idle	

## HDLC Communications

Model type	PEX-H467102	PEX-H467102P
Number of channels	2 channels	
Communications standards	RS-485 (TIA/EIA-485)/RS-422 (TIA/EIA-422) standard	
Control signals	T, C, R, I, ST, RT	
Base clock frequencies*1	8.192 MHz, 12.288 MHz, 14.7456 MHz, 19.6608 MHz, 32 MHz, 49.152 MHz, 58.9824 MHz	
Transfer rates*2	4 Mbps (Internal clock receiving: 2 Mbps)	
Communications mode	HDLC	
HDLC controller	Our original HDLC controller	
Transceiver	MAX3491E (Maxim Integrated Products, Inc.) or equivalent	
Full-duplex communications/ half-duplex communications	Software programmable	
Termination Resistor	Connected/unconnected (DIP switch selectable)	
Transmit/receive buffer memory	1 MB per channel	
Maximum transmission length	1,000 m (depending on the transfer rate and wiring environment)	

Note:

- \*1 You can choose an appropriate base clock frequency for each communications channel.
- \*2 Refer to the manual for more details.



Interface Amita Solutions, Inc.

E-mail: [sales@amitasol.com](mailto:sales@amitasol.com)

Website: <http://www.interface-amita.com>

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